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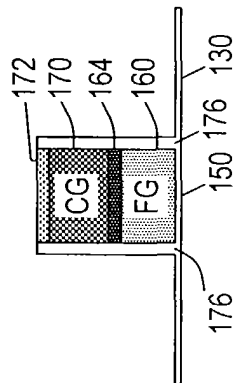


FIG. 1

PRIOR ART

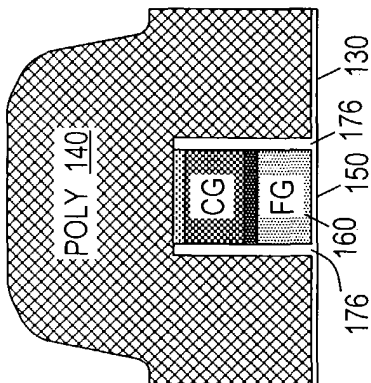


FIG. 2

PRIOR ART

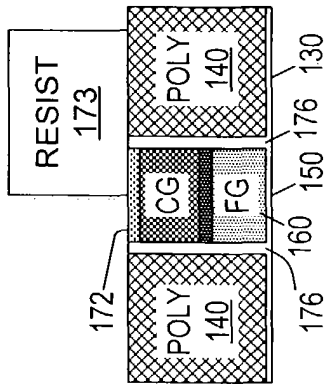


FIG. 3

PRIOR ART

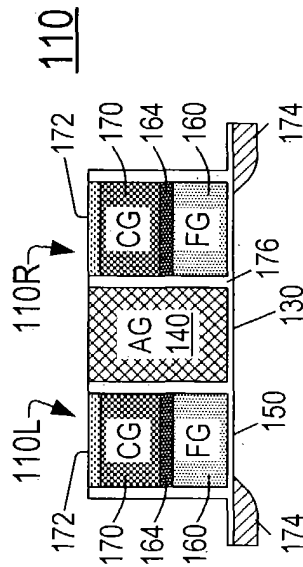


FIG. 5

PRIOR ART

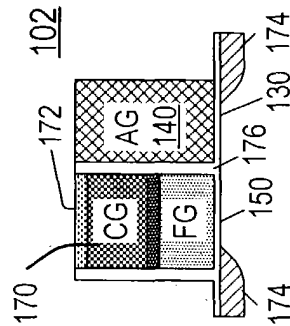
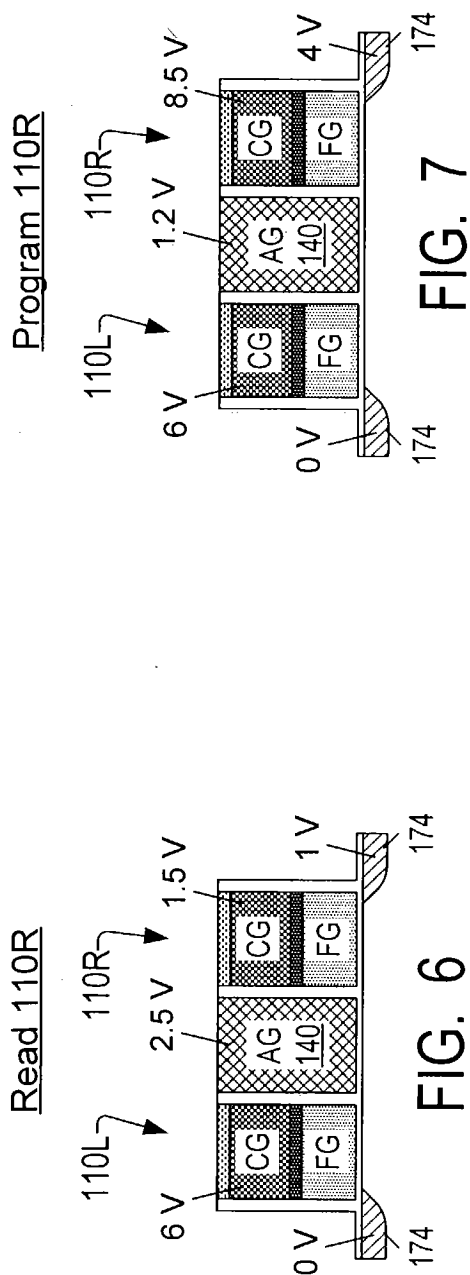


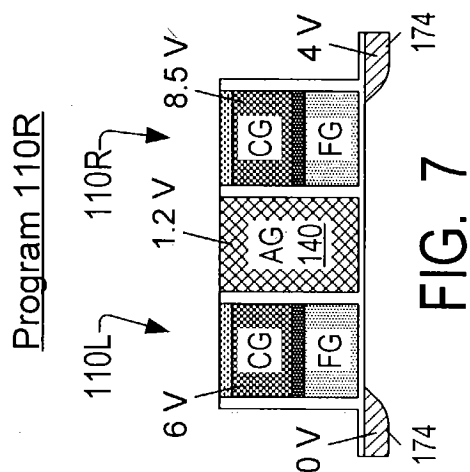
FIG. 4

PRIOR ART

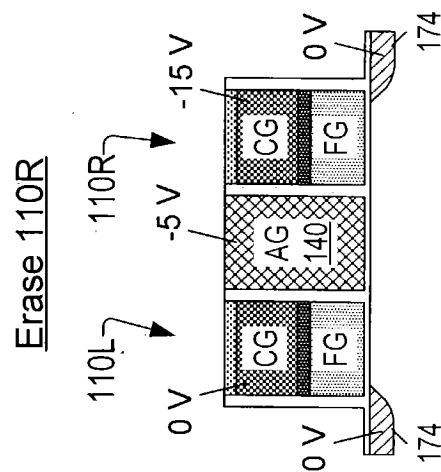
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PRIOR ART



PRIOR ART



PRIOR ART

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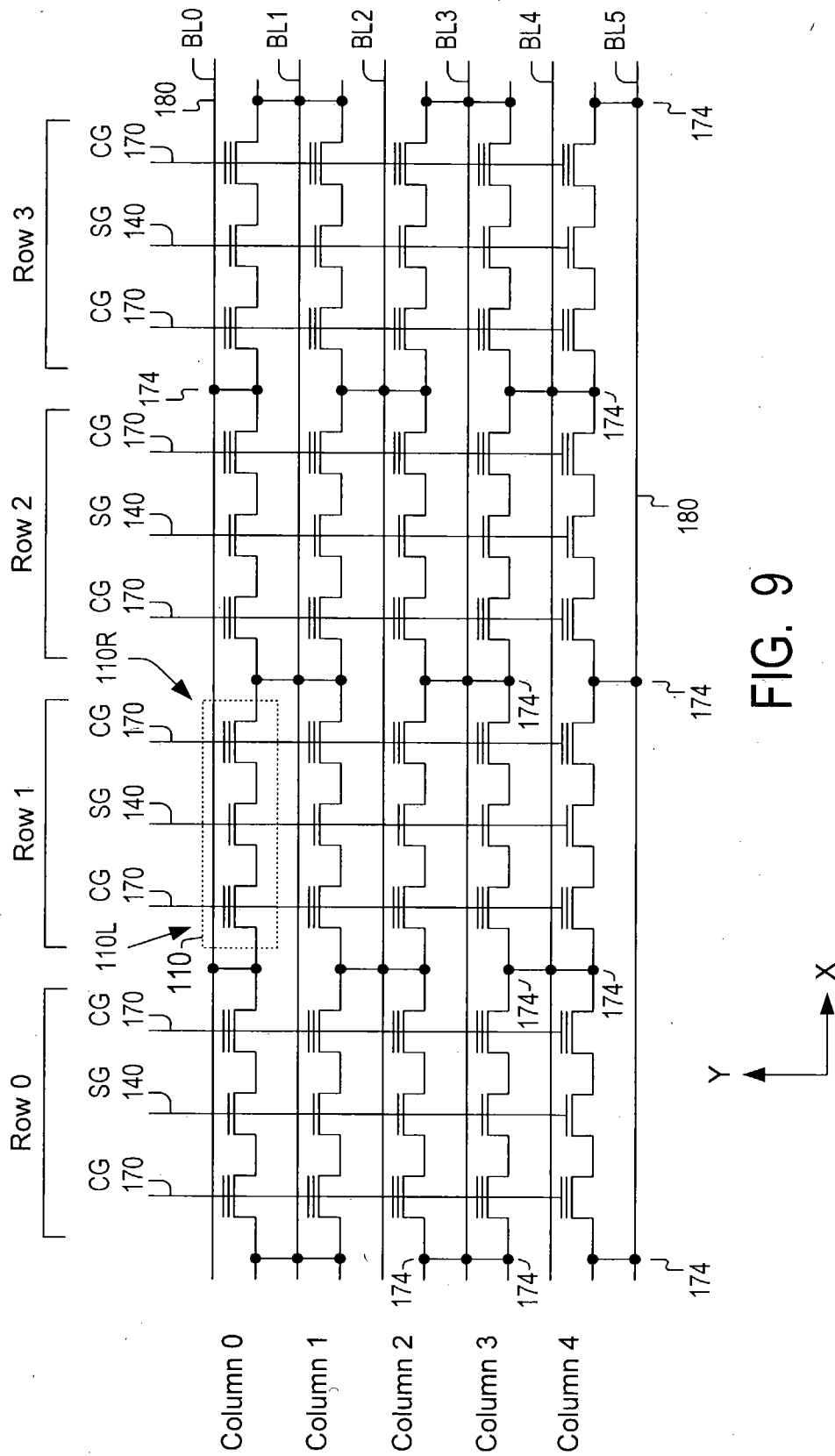
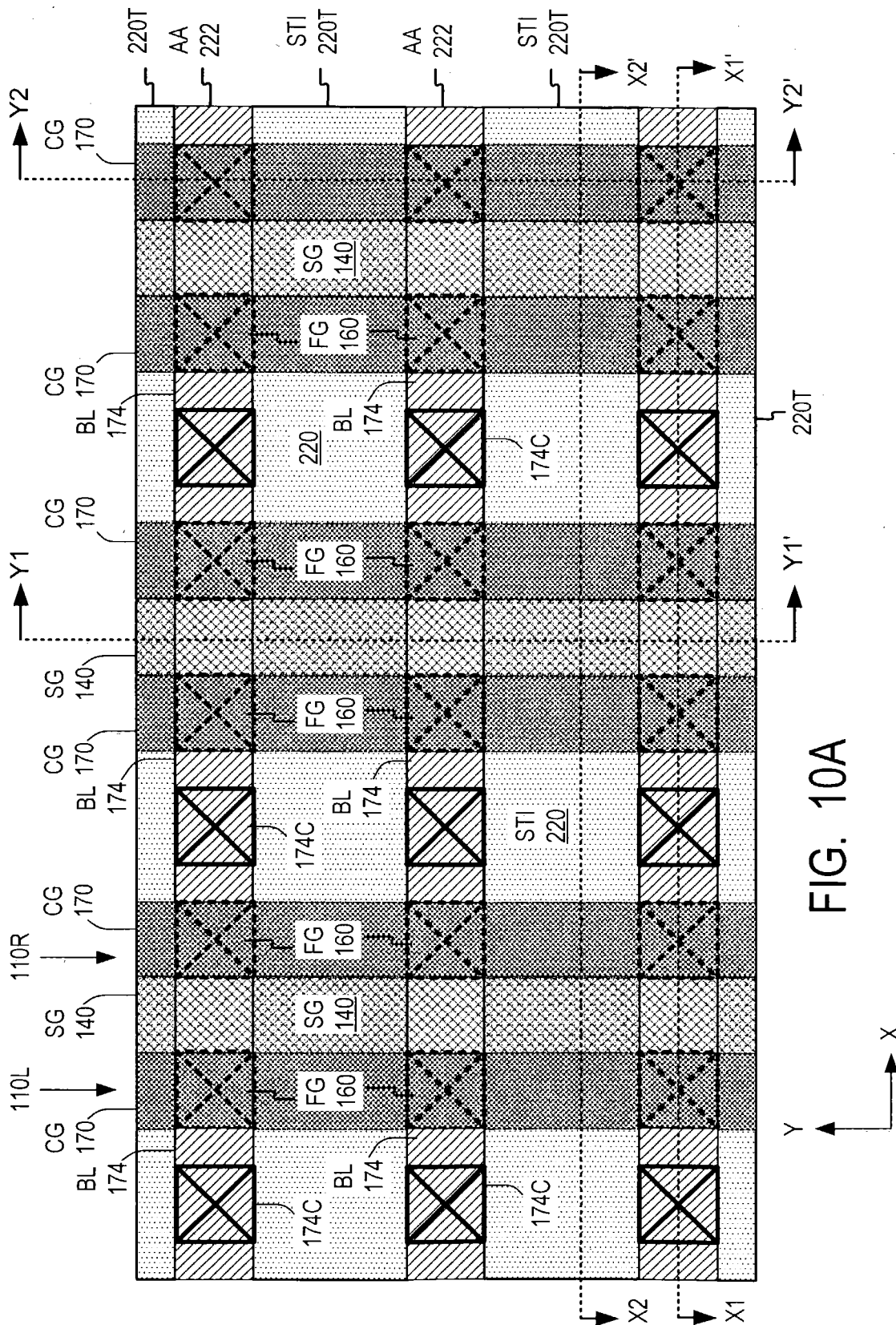


FIG. 9

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FIG. 10B

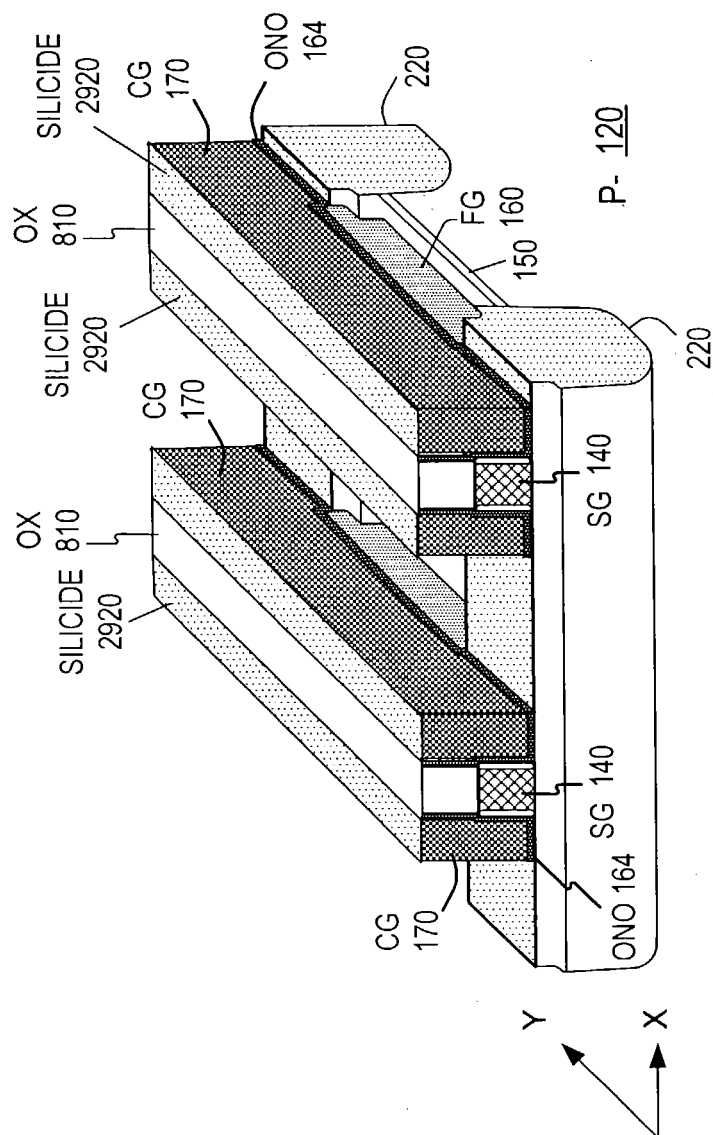
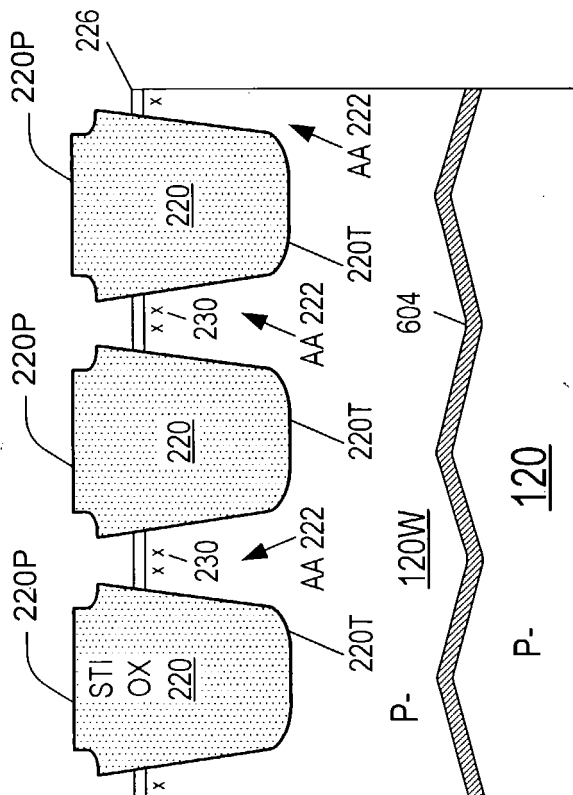


FIG. 11 (Y1-Y1')



**FIG. 12B**

(PERIPHERY)

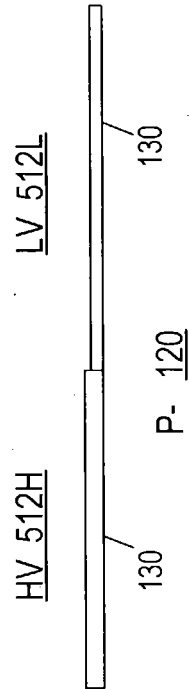
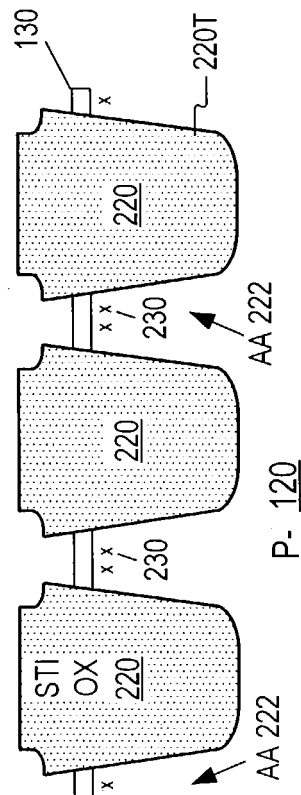


FIG. 12A (Y1-Y1')



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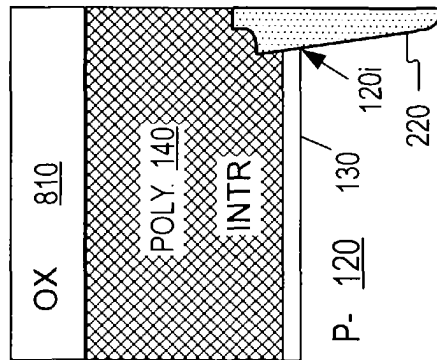


FIG. 13B  
(PERIPHERY)

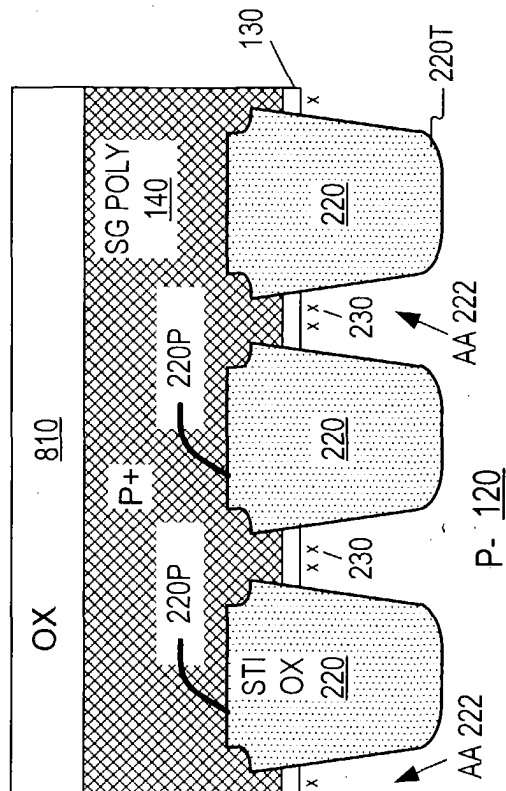


FIG. 13A (Y1-Y1')

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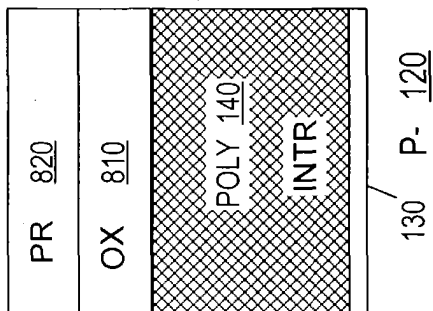
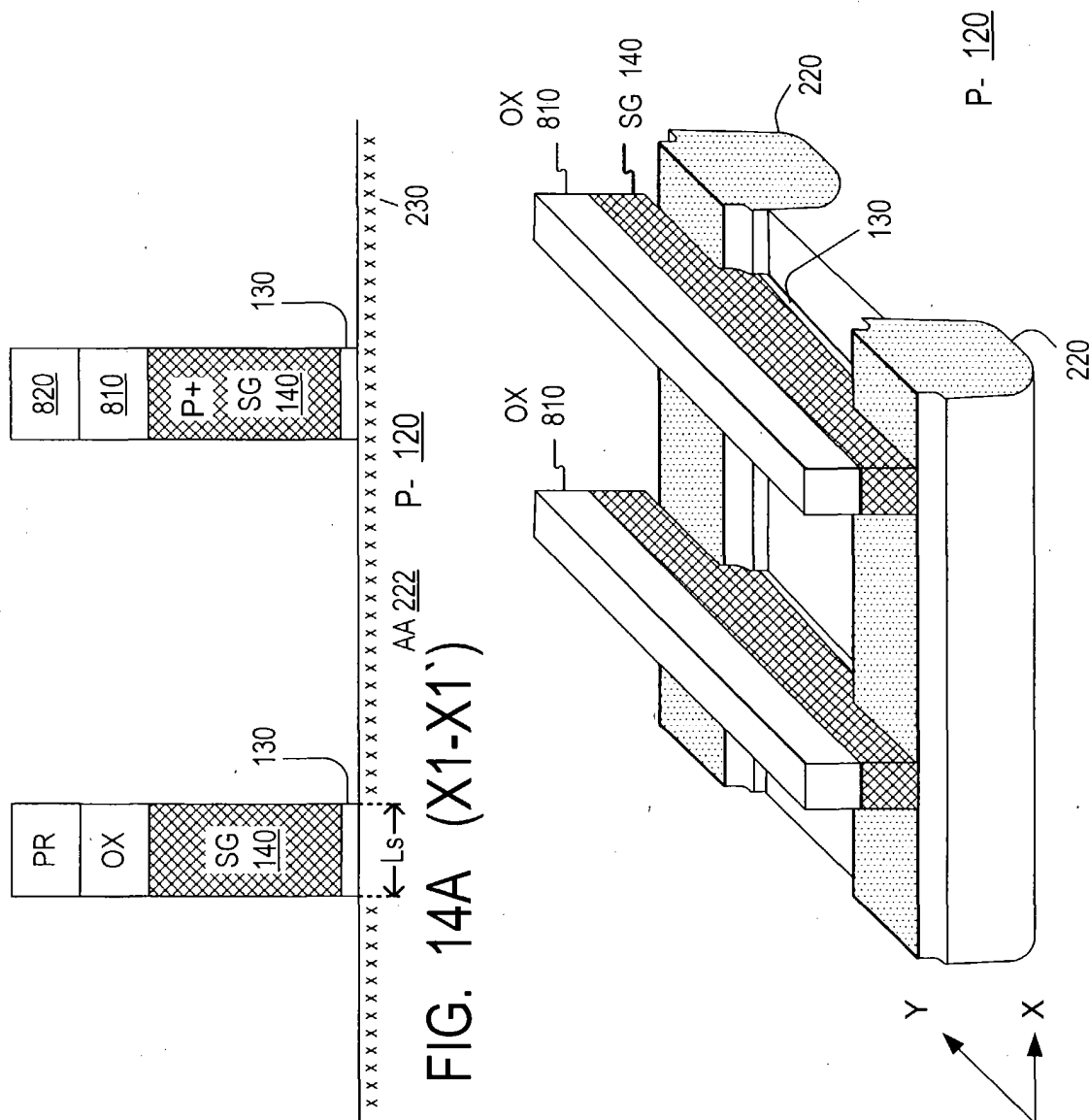


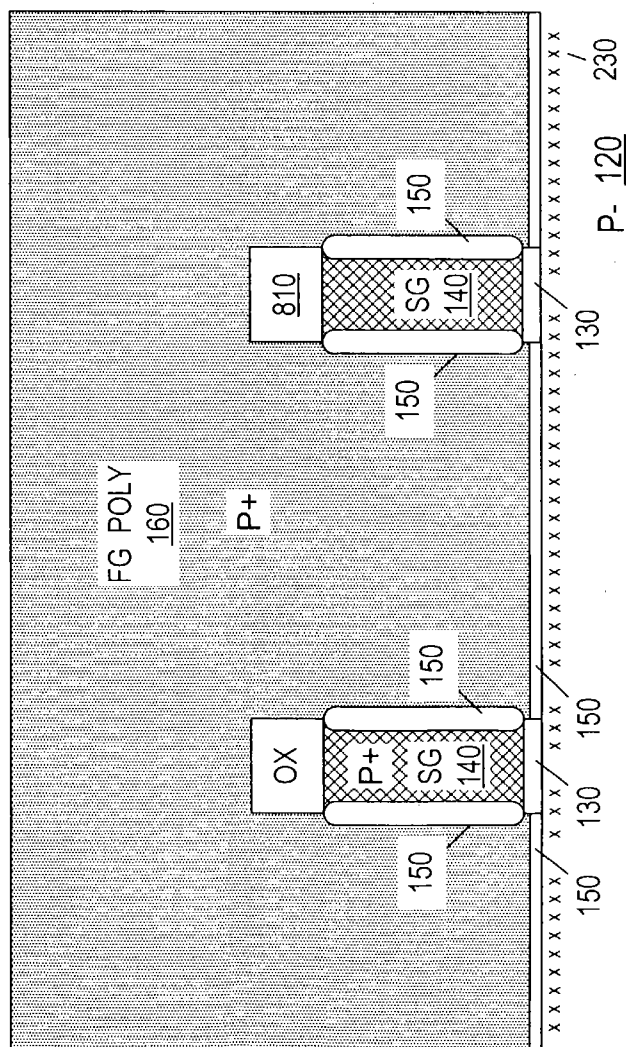
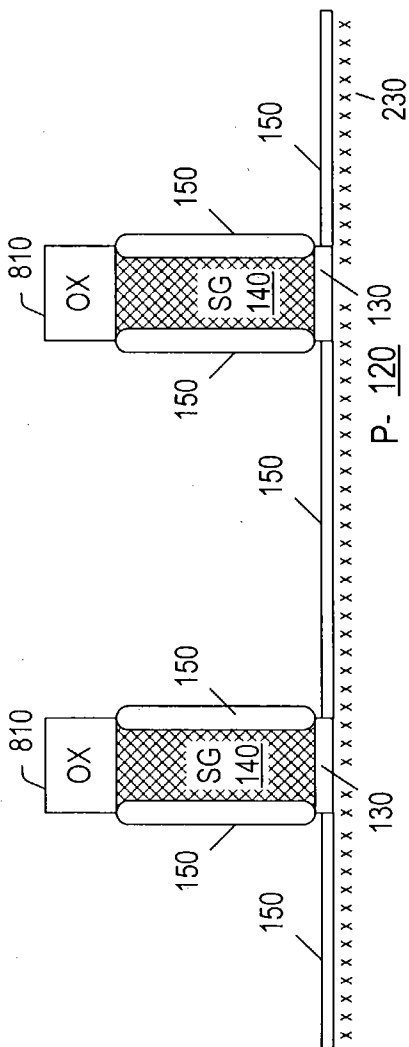
FIG. 14B  
(PERIPHERY)

FIG. 15





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FIG. 18  
(X1-X1')

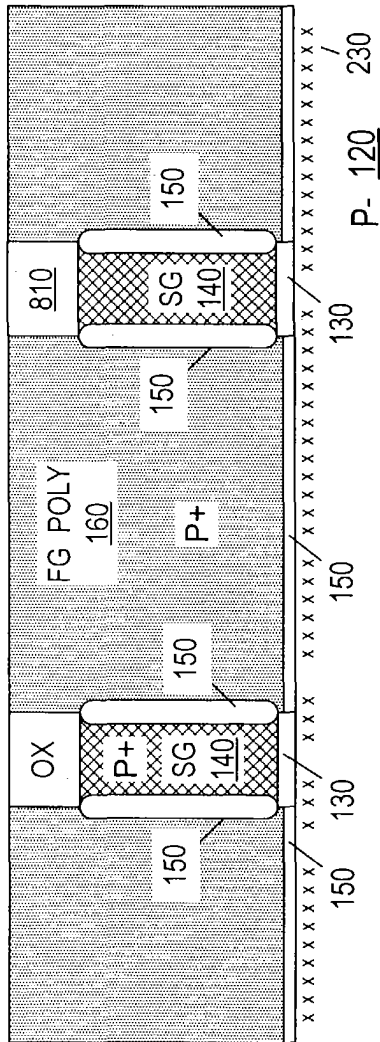


FIG. 19A  
(X1-X1')

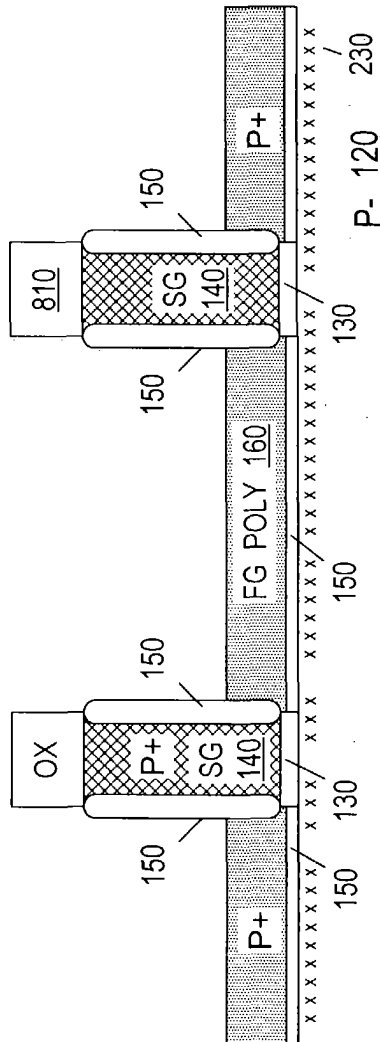
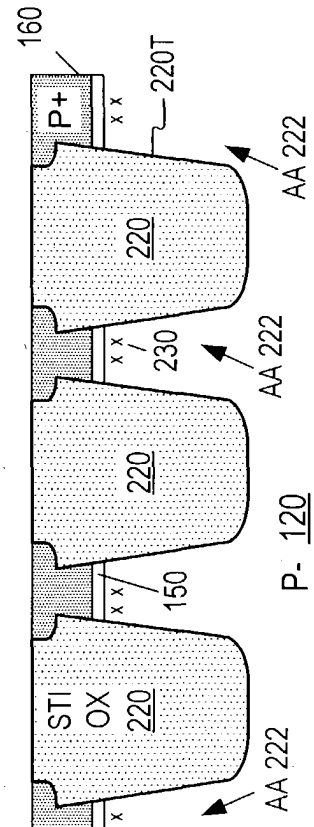
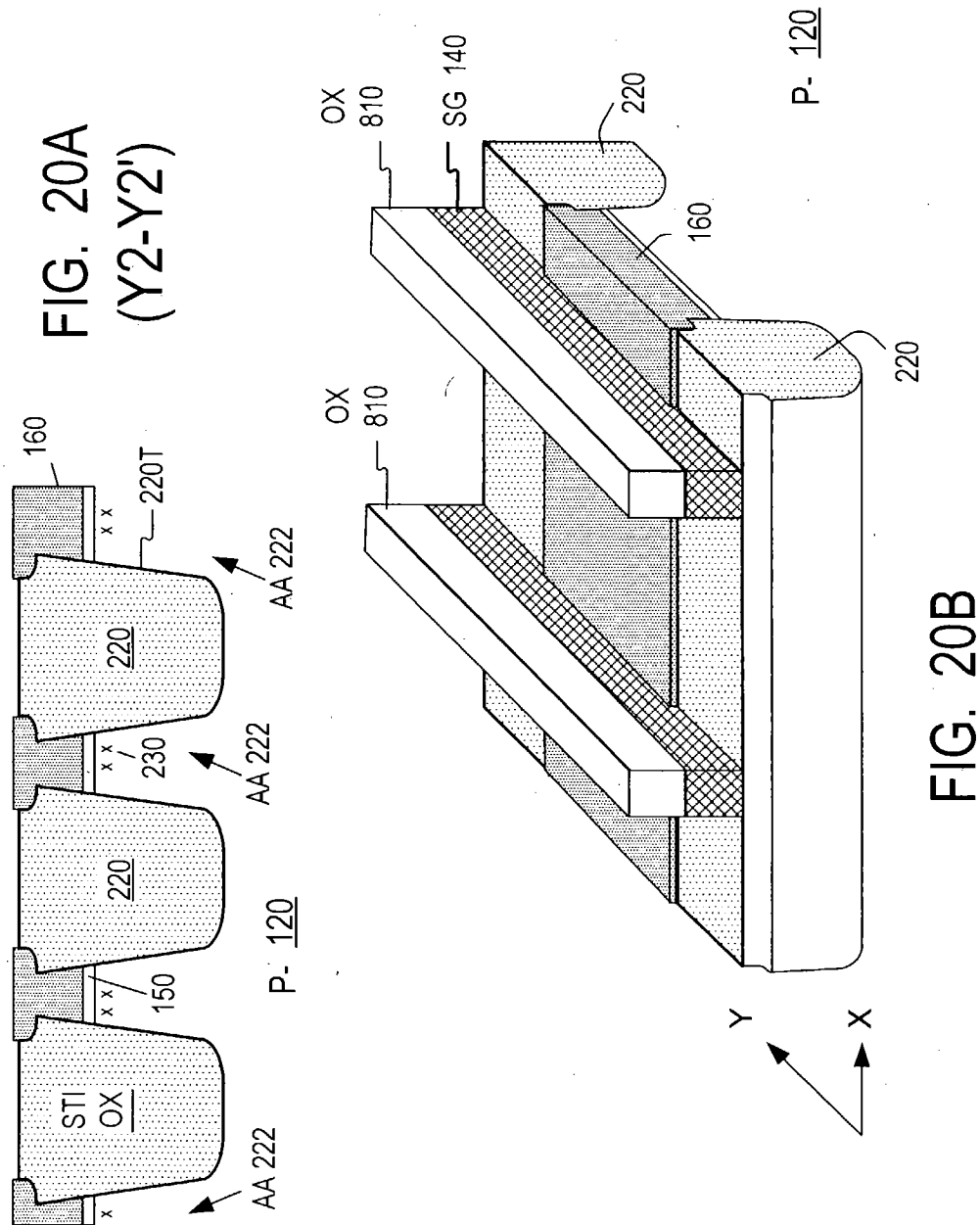


FIG. 19B  
(Y2-Y2')



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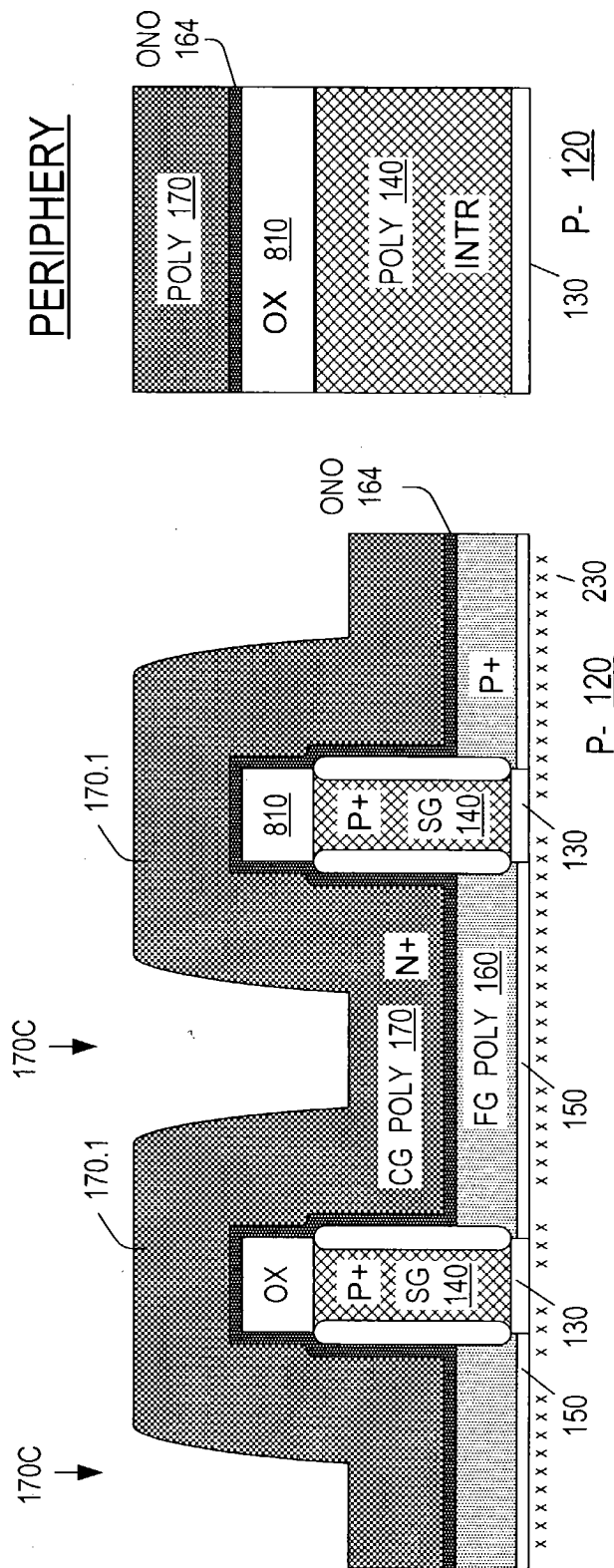


FIG. 21B

FIG. 21A (X1-X1')

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FIG. 22  
(X1-X1')

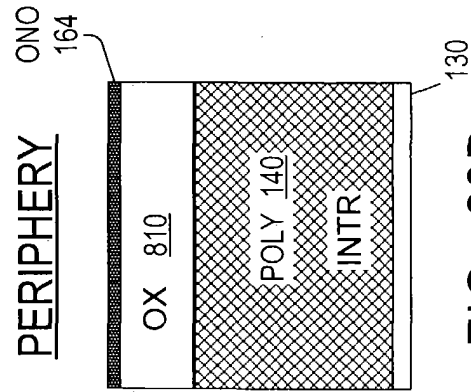
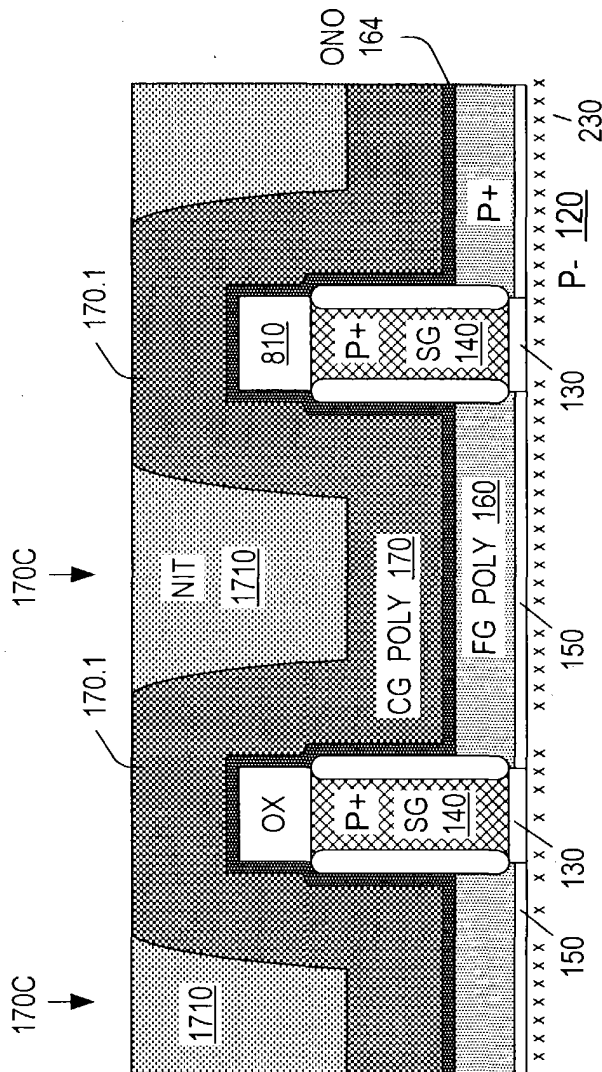


FIG. 23B

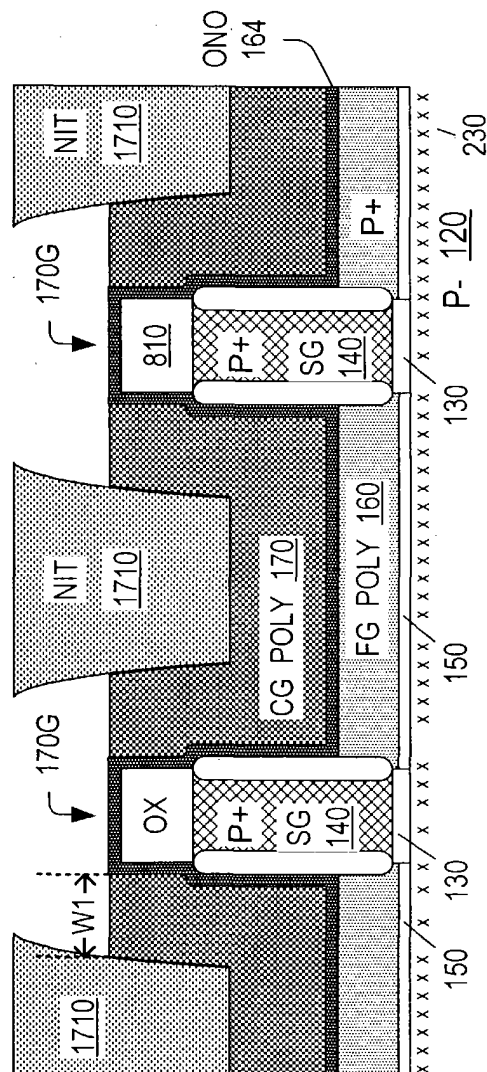


FIG. 23A (X1-X1')

FIG. 24  
(X1-X1')

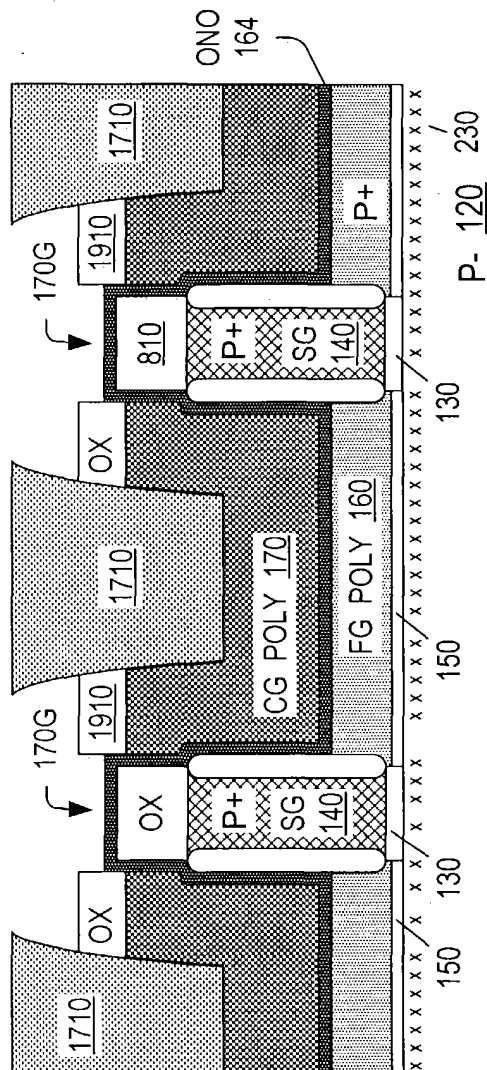
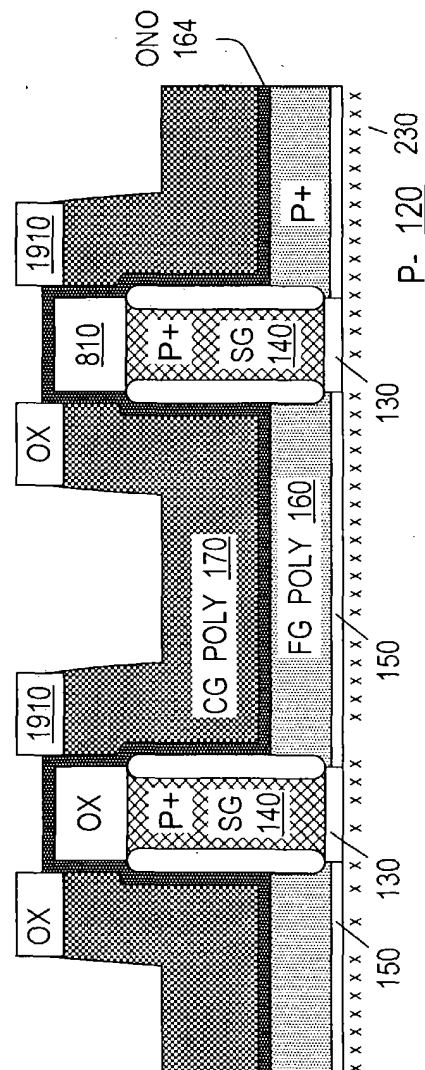


FIG. 25  
(X1-X1')



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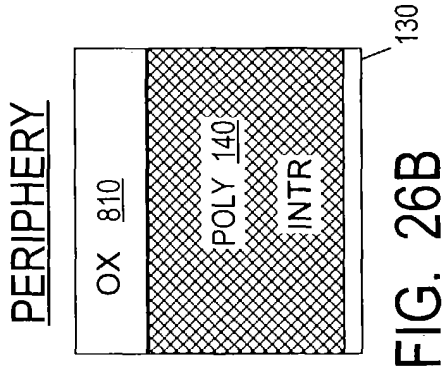
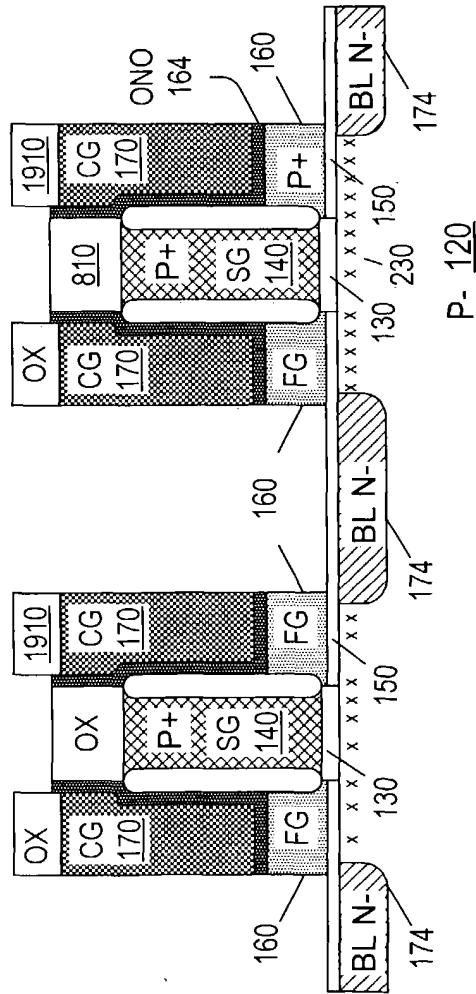
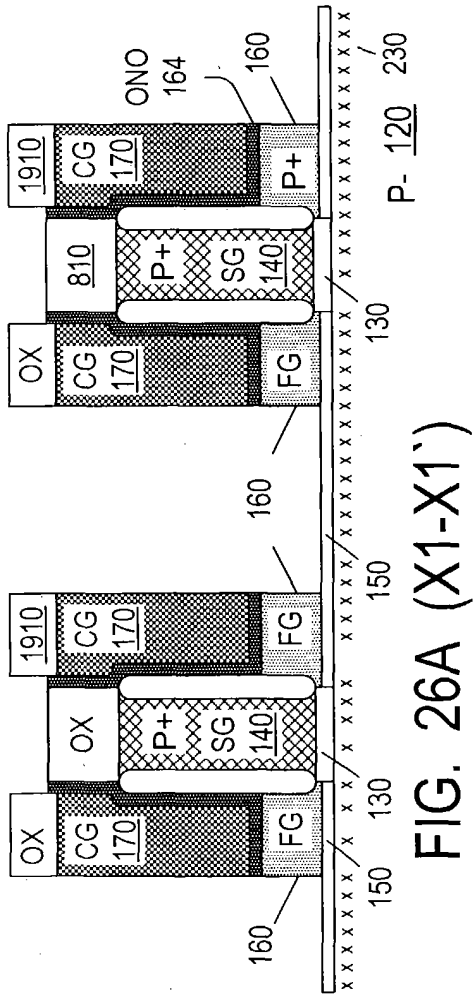


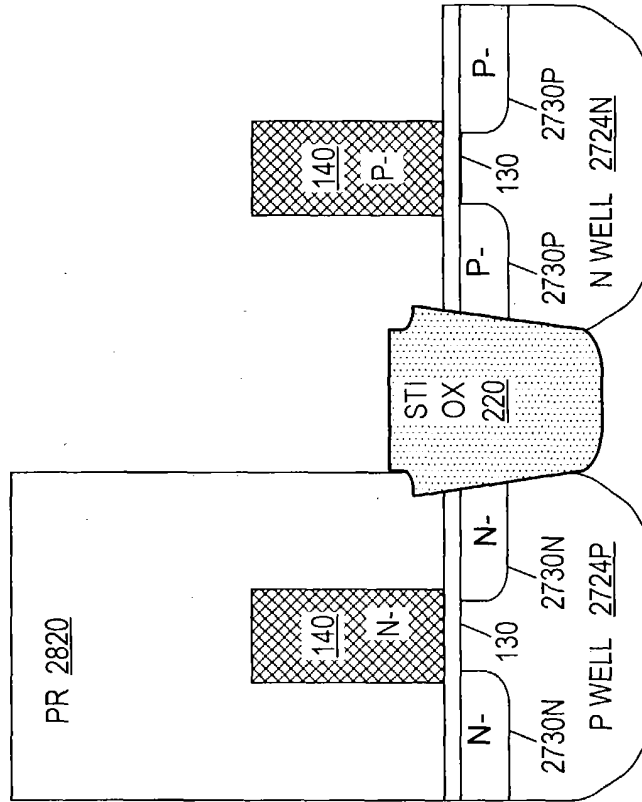
FIG. 27A  
(X1-X1')



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NMOS 512N

PMOS 512P

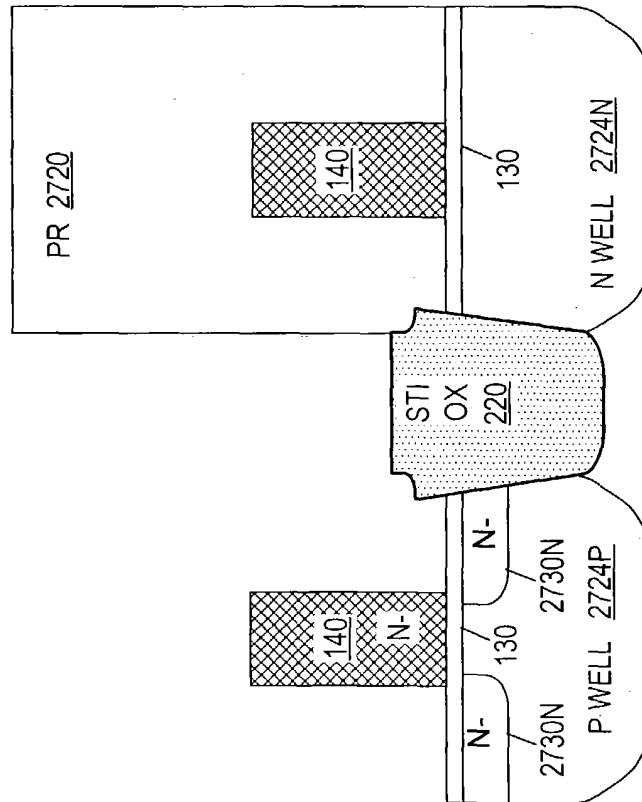


P- 120

FIG. 28  
(PERIPHERY)

NMOS 512N

PMOS 512P



P- 120

FIG. 27B  
(PERIPHERY)





FIG. 30A  
(X1-X1')

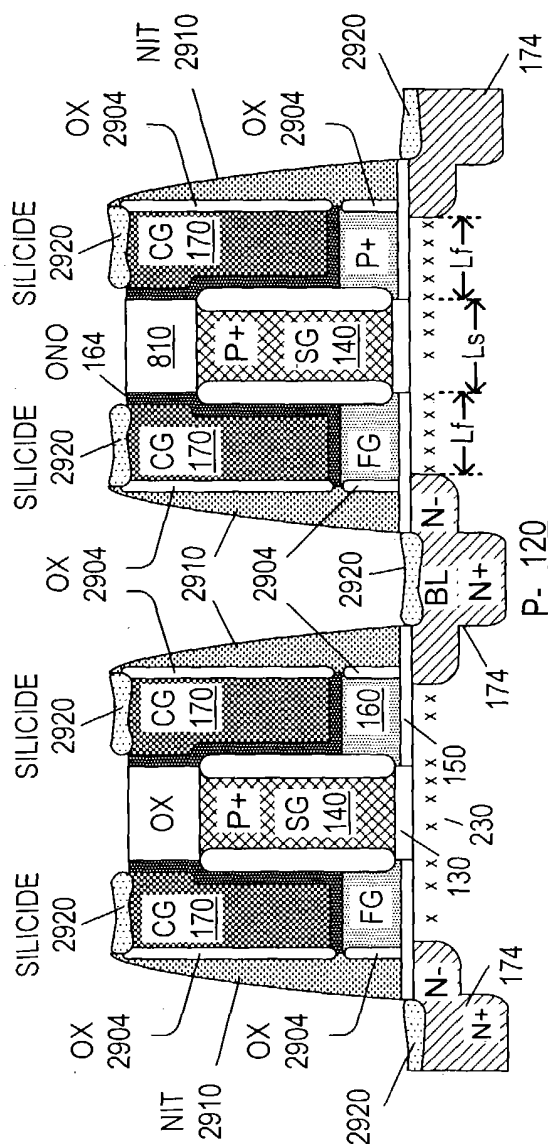
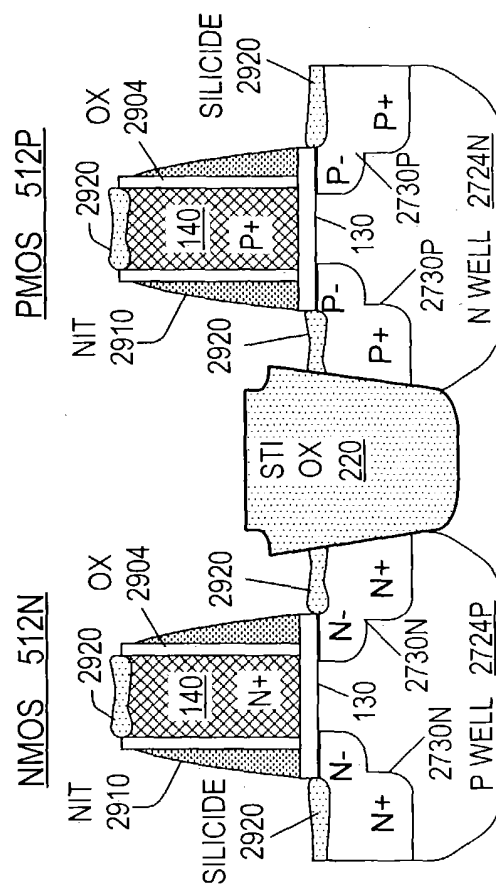
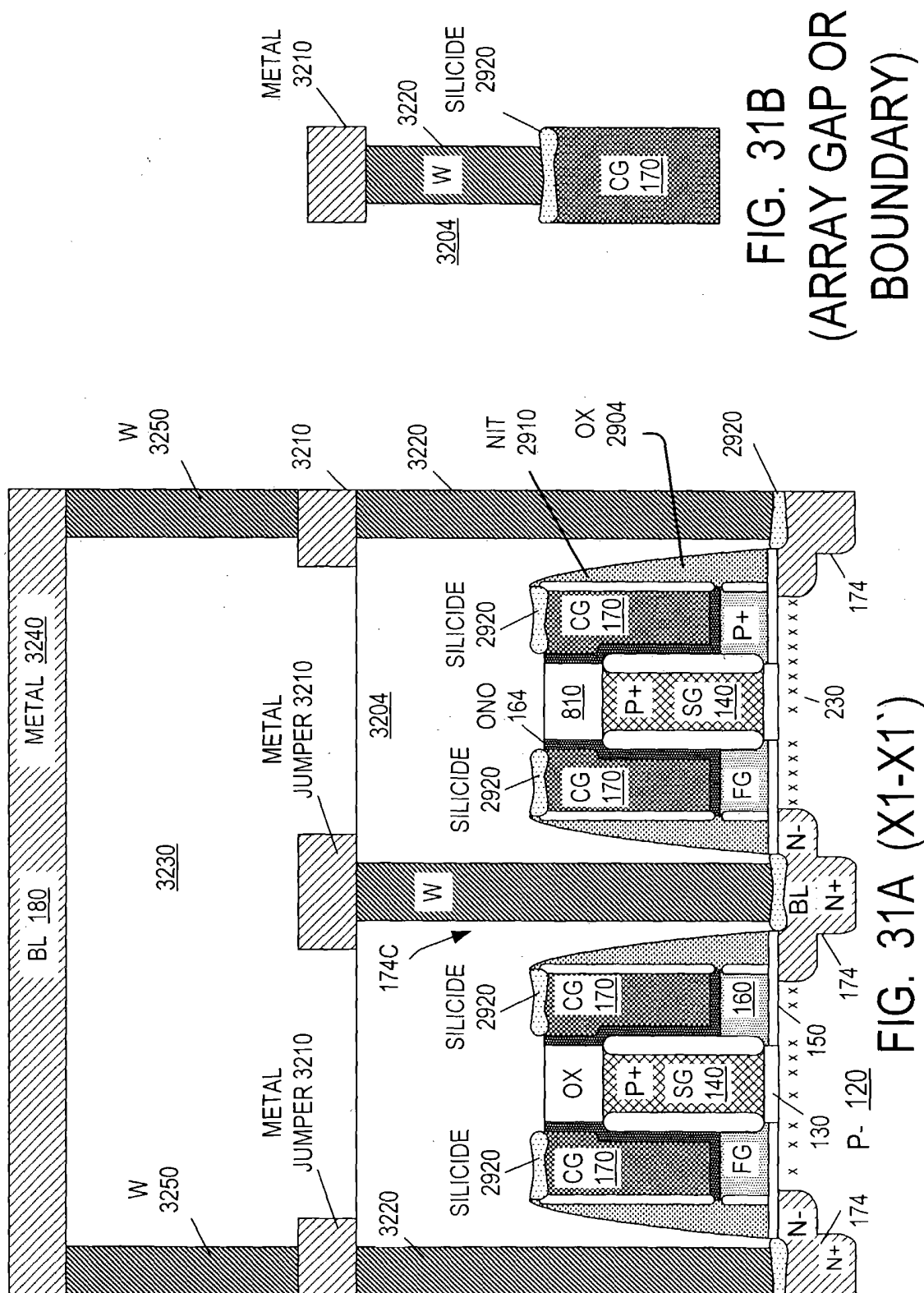


FIG. 30B  
(PERIPHERY)





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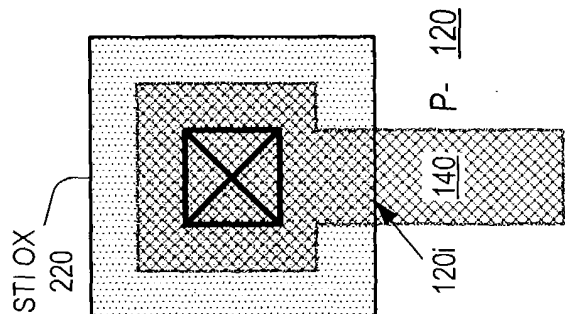


FIG. 31E  
(PERIPHERY)

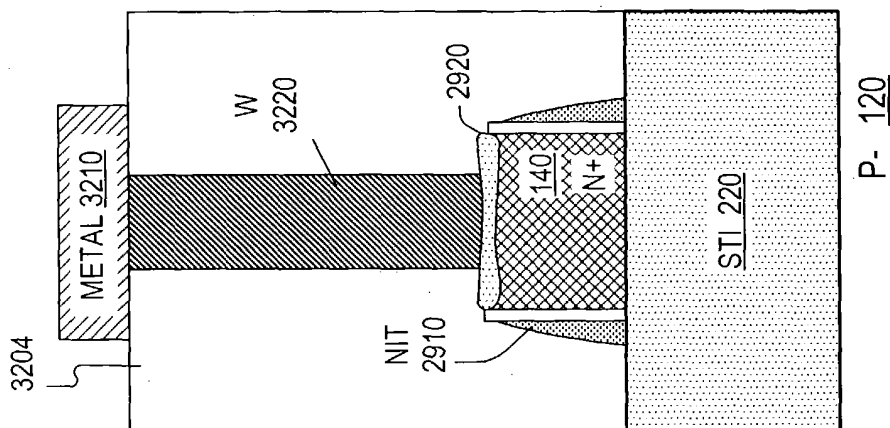


FIG. 31D  
(PERIPHERY)

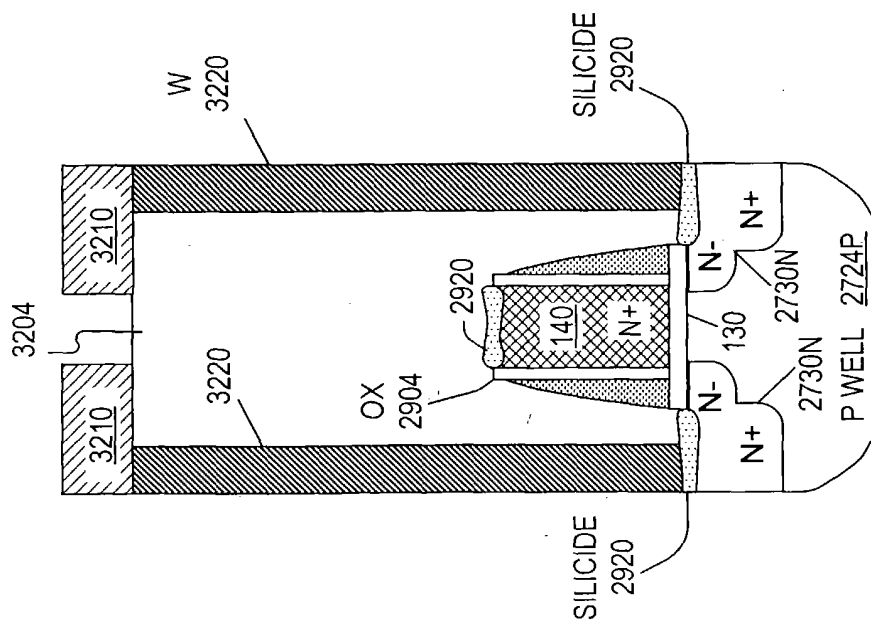


FIG. 31C  
(PERIPHERY)

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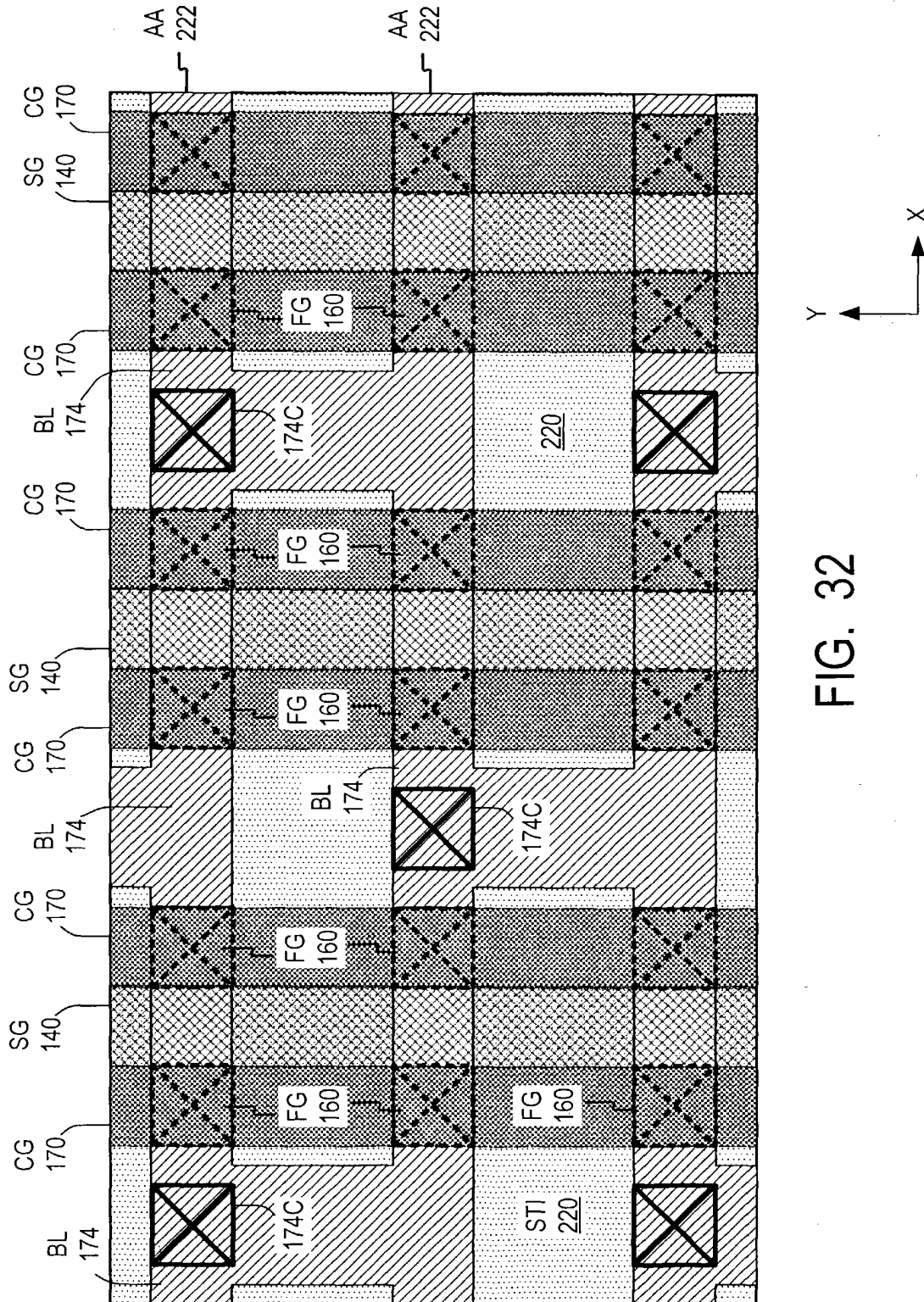


FIG. 32